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MOSFET DEVICES FOR ENERGY EFFICIENCY



Te-Kuang Chiang received the Ph.D. degree from the National Cheng Kung University, Tainan, Taiwan. Since 2008, he has been with the Department of Electrical Engineering, National University of Kaohsiung, Kaohsiung, Taiwan, where he is a full Professor. He has authored/co-authored over 100 conference/journal papers. His researching work puts focus on the VLSI device modelling and simulation, where the modelling device is primarily composed of multiple-gate device structure, such as tri-gate MOSFET, surrounding-gate MOSFET, omega-gate MOSFET, Pi-gate MOSFET, and Inverted-T-shaped-gate MOSFET. Also, Dr. Chiang served as the IEEE EDS Tainan Chapter Vice Chair (2009 ~), the IEEE EDS Tainan Chapter Treasurer (2007 ~), the IEEE representative to the EDS Regions/Chapters Committee (2008 Jan.~), the IEEE EDS AdCom Members (2002~), the IEEE Member (2002~), and the IEEE EDS Member (2007~). In the past ten years, he also organized IEEE Conference, joined the committee, and gave an invited talk. He was the Technical Program Chair, IEEE International Conference on Electron Devices and Solid-State Circuits, 2014, Cheng-Dou China, the Technical Program Committee, IEEE International Conference on Electron Devices and Solid-State Circuits, 2013, Hong Kong, the Technical Program Committee, IEEE International Conference on Electron Devices and Solid-State Circuits, 2012, Bangkok, the Technical Program Chair, IEEE International Symposium on next electronics, 2012, Kaohsiung, Taiwan, the Technical Program Chair, IEEE International Symposium on next electronics, 2010, Kaohsiung, Taiwan, Technical Program Committee Member, IEEE International Conference on Solid-State and Integrated Circuits Technology, 2010, Beijing, China, International Advisory Committee, 2009, IEEE International Conference on Electron Devices and Solid-State Circuits, Xian, China.

